

## Biography



**Dr Xiaojun Wang** received his BEng in computer and communications from Beijing University of Posts and Telecommunications (BUPT), China in 1984 and his MEng in computer applications in 1987 also from BUPT. Xiaojun was employed as Assistant Lecturer/Lecturer in BUPT from 1987 to 1989. He received a PhD scholarship from the Sino-British Technical Co-operation Training Award in 1989, and did his PhD research in the School of Engineering in [Staffordshire University](#)<sup>1</sup> (then Staffordshire Polytechnic), England, UK from 1989 to 1992. He was awarded a PhD for his research in the area of High Level Logic Synthesis from Staffordshire University in 1993. He joined the School of Electronic Engineering of Dublin City University as an assistant Lecturer in November 1992, permanent staff in 1995, and he is now a senior lecturer. He is a member of [the IET](#)<sup>2</sup>. He was Head of China Affairs in Dublin City University for five years between 2002 and 2007.

He is the main initiator and organizer of the China-Ireland International Conference in Information and Communications Technologies ([CIICT](#)<sup>3</sup>) which has been held five times from 2006 to 2010 alternating between China and Ireland. He made major contributions to the establishment of the [China connections](#)<sup>4</sup> for the School of Electronic Engineering and played a key role in the [China operation](#)<sup>5</sup> of Dublin City University. He is the coordinator of a successful collaborative masters program in Telecommunications Engineering between DCU and Wuhan University.

Dr Wang is interested in the area of low power electronics design, and energy efficient ICT. He currently focuses on energy efficient networking and, hardware acceleration for packet classification and deep packet inspection. He is director of the Network Processing Group in the [Network Innovations Centre \(NIC\)](#)<sup>6</sup> in the [Rince Institute](#)<sup>7</sup>.

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1. <http://www.staffs.ac.uk>
  2. <http://www.iee.org.uk/>
  3. [www.ciict.org](http://www.ciict.org)
  4. <http://www.eeng.dcu.ie/china/g1/257-EE.html>
  5. <http://www.dcu.ie/international/chinese/index.shtml>
  6. <http://www.eeng.dcu.ie/nic>
  7. <http://www.rince.ie>

## Teaching Duty

- I teach the following [courses](#)<sup>8</sup>
  - **EE213** - VLSI Design (1998 - 2000, **2007** - )
  - EE317 - Engineering Computation and Simulation (1996 - 2002, 2004 - 2007)
  - **EE404** - Digital Electronics (1996 - 2002, **2009**- )
  - **EE540** - HDL and High Level Logic Synthesis (**1994** - )
    - [HDL and High\\_Level Logic Synthesis](#)<sup>9</sup>
    - This module is also available through the [RACeE](#)<sup>10</sup> (Remote Access to Continuing engineering Education) program
  - **EE548** - Internetwork Security (2010 - )

I've also taught other modules including Operating Systems, Mobile Robotics (Interactive C part), and first year Digital Logic Circuits (lab part).

## Research Interests

For research information, visit the [Network Processing Group \(NPG\)](#)<sup>11</sup>

## Contact Information

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[DCU School of Electronic Engineering Homepage](#)<sup>14</sup>

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11. <http://www.eeng.dcu.ie:8888/EE/471-EE.html> (Network Processing Group (NPG))

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14. <http://www.eeng.dcu.ie>