Masters and Masters Qualifier in Electronic Systems and Telecommunications Engineering

Plagiarism, Copying and Collusion

- Plagiarism is a **serious offence** in the university.
- Familiarise yourself with the ‘DCU Academic Integrity and Plagiarism Policy’
- Many/most CA submissions will use plagiarism detection software (Turnitin)
- Local processes applied 2014-15 = 10
- Cases referred to University Disciplinary Committee 2014-15 = 2
What is considered unacceptable?

• Paying others to do your assignments
• Submitting your own work twice for different assessments
• Falsifying results or outcomes
• Providing false documentation
• Reproducing work of another person without appropriate acknowledgement/referencing
• Working with other students to submit material that is required to be an individual effort
What happens if you breach regulations?

- **Local Process**
  - Interview
  - Outcome
  - Penalty if upheld – up to **0% for entire module!**

- **University Disciplinary Committee**
  - Substantial breach
  - Second breach
  - Committee hearing
  - Penalty if upheld – up to **expulsion** from Programme/University
Examples of Unacceptable Practice

• Direct use
  – If you use someone else’s words, you must use *quotation marks* AND *cite the original source*
  – You should not need to directly use more than a single sentence at a time!

• Paraphrasing
  – If you use someone else’s work or ideas, you must still use a *reference to the original source*

• Acceptable but poor academic quality
  – Lots of quotation pieces, lots of ideas correctly referenced, figures included that are correctly referenced *but* little student contribution
  – Masters student are expected to make **critical analysis**

http://www.lib.usm.edu/legacy/plag/plagiarismtutorial.php
Project

• Coordinator – Jennifer Bruton (me again!)
• All activities will take place on the Loop site
  – Updated for 2015-16 over next week
• Please use your Project Code with all communications
• You **must** be registered to be allocated a project
Project Registration

• Every student intending to graduate in 2015/16 should complete **ONE** of the following Masters project modules
  - EE592: Electronic Systems Project (MEN)
  - EE593: Telecommunications Eng. Project (MTC)
  - EE594: Nano Major Project (MEN-Nano)
  - EE595: Imaging Major Project (MEN-IPA)
  - EE596: Network Major Project (MTC-Network)
  - EE598: Semi-conductor manufacturing (MEN-SEMI)

• Graduation for 2015/16 will take place in March 2017
• Transcripts are issued in November 2016
• Letters to foreign institutions may be issued in Sept. 2016
Project Registration – Part-Time Students

Full-Time Students:

September Start:

Semester 1  Semester 2  Summer

↑ Initial Taught Module Registration

Project Activities

↑ Project Registration

Part-Time Students:

September Start:

Semester 1  Semester 2  Summer  Semester 1  Semester 2  Summer

↑ Initial Taught Module Registration

Project Activities

↑ Project Registration
Project Schedule

- **Selection & Allocation**: End October
- **Literature Review Submission**: Sem 2 Week 1 (Week 19)
- **Oral Presentation**: Mid February
- **Project Design Completion**: Mid June
- **Independent Implementation**: End June - Mid Aug
- **Testing, Critical Analysis & Writing**: Mid Aug - Sept (Week 1)
- **Portfolio Submission**: September (Week 1)
- **Final Interview & Assessment**: Mid September
- **Earliest Possible Graduation**: March
Project Assessment

• Project submissions are subject to Turnitin plagiarism detection software

• Marking Outline (Provisional)
  – Literature Review 10%
  – Oral Presentation 10%
  – Final Portfolio 70%
  – Project Planning & Discipline 10%

• A failing mark in the Final Portfolio is used as the overall module mark

• There is no RESIT, there is 1 REPEAT allowed
Success

The Programme Team want you to succeed!

- Plan
- Select carefully
- Manage your time
- Engage with your modules/peers/lecturers
- Check for information
- Communicate
- Work hard
- Prepare

Enjoy your learning experience at DCU!